CDF Run II Silicon Vertex Detector Annealing Study

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Abstract

Between Run II commissioning in early 2001 and the end of operations in September 2011, the Tevatron collider delivered 12 fb⁻¹ of $p\bar{p}$ collisions at $\sqrt{s}=1.96$ TeV to the Collider Detector at Fermilab (CDF). During that time, the CDF silicon vertex detector was subject to radiation doses of up to 12 Mrad. After the end of operations, the silicon detector was annealed for 24 days at 18°C. In this paper, we present a measurement of the change in the bias currents for a subset of sensors during the annealing period. We also introduce a novel method for monitoring the depletion voltage throughout the annealing period. The observed bias current evolution can be characterized by a falling exponential term with time constant $\tau_I = 17.88 \pm 0.36 \pm 0.25$ days, in good agreement with Hamburg-model predictions. For the depletion voltage, we observe an average decrease of $(27 \pm 3)\%$ in the depletion voltage, whose evolution can similarly be described by an exponential time constant of $\tau_V = 6.21 \pm 0.21$ days. The depletion voltage results both deviate from the Hamburg-model predictions by roughly one standard deviation.

Keywords: Annealing, Silicon, Vertex Detector, CDF, Detector Operations

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1. Introduction

In high-energy physics (HEP) experiments, silicon sensors serve a crucial role in the detection of charged particle positions, momenta, and to some extent, dE/dX information. Due to their typically close location to the collision point of hadron colliders, silicon sensors often incur intense radiation damage due to the numerous particles from collisions that traverse them. The macroscopic effects of radiation damage on silicon sensors in HEP detectors has been extensively studied. The leakage currents increase linearly with radiation dose, and for n-bulk sensors, the depletion voltage $V_{\rm dep}$ initially decreases until the sensor appears to undergo type inversion, at which point $V_{\rm dep}$ then increases with radiation dose. These macroscopic changes have been linked to the formation of crystal defects when atoms are displaced from their lattice positions after collisions with particles from the radiation field.

The process of annealing is the opposite effect, where increasing the temperature of the silicon sensor allows the displaced atoms to settle back into a vacant lattice position, eliminating the local crystal defect, and at least partially recovering some of the initial behavior of the silicon sensor as it was before irradiation. Annealing, which is strongly temperature dependent, has been studied with test sensors, where the irradiation phase and the annealing phase can be isolated from each other by strict temperature controls. Such temperature control enables the construction of silicon-behavior models which can closely approximate ideal silicon sensor behavior. The most popular of these is the *Hamburg model* [1, 2] and verification of it and other models is still ongoing.

Because annealing can prolong the life of a HEP silicon detector, understanding how the macroscopic quantities such as leakage current and depletion voltage change with time for different temperatures is of great interest to the HEP silicon detector community. Test bench studies are usually done at warm (40-80°C) temperatures to maximize the annealing effect in the available time, while annealing of HEP detectors is more practical at room temperature.

This article describes the annealing studies that were performed with the silicon detector system at the CDF experiment at Fermi National Accelerator Laboratory. The silicon sensors were exposed to 0.4-12 Mrad of radiation over the course of 10 years, and dedicated annealing studies were performed after the end of Tevatron Run II. This *in-situ* measurement of annealing with an operating HEP detector required a new method for monitoring the depletion voltage of the sensors. We discuss some annealing theory in Sec. 2 and the detector in Sec. 3. The measurement and monitoring procedures are detailed in Sec. 4. The analysis of the

current changes and depletion voltages are given in Secs. 5 and 6 and the results and conclusions follow in the remaining sections.

40 2. Silicon Annealing

The behavior of a silicon detector can be characterized by several quantities. For this study, we consider the leakage current, and the depletion voltage $V_{\rm dep}$, which for an unirradiated sensor is defined as the minimum bias voltage applied to the sensor that can deplete it of free charge carriers. As the silicon sensors are irradiated, the behavior of these quantities changes. The leakage current increases in a manner linearly proportional to the fluence:

$$\Delta I = \alpha \Phi_{eq} V \tag{1}$$

where α is the current related damage rate, Φ_{eq} is the fluence, and V is the volume of the sensor. ΔI is the increase in leakage current from its original value I_0 . The magnitude of α is temperature-dependent and on the order of 10^{-17} A/cm.

During annealing, the leakage current is observed to decrease and the rate of this decrease strongly depends on temperature, based on studies performed in the temperature range 0-60°C. The decrease of the leakage current is often parameterized according to the Hamburg model, which suggests a leakage-current evolution according to the formula:

$$\Delta I(t) = \Delta I(t_0) \sum_{i} b_i \exp\left(-\frac{t}{\tau_i}\right) . \tag{2}$$

In this expression, t_0 represents the start time of annealing and the sum is over different types of crystal defects, each of which has a temperature-dependent characteristic time constant τ_i and an amplitude b_i , subject to the constraint $\sum_i b_i = 1$. Table 1 shows characteristic values for the constants b_i and τ_i for the annealing

Term i	1	2	3	4	5	6
τ_i at 18 °C (days)	1.68×10^{-2}	1.12×10^{-1}		13.9	83.7	∞
τ_i at 11 °C (days)	4.99×10^{-2}	3.32×10^{-1}	3.05	41.4	249	∞
τ_i at -5 °C (days)	7.46×10^{-1}	4.97	45.5	619	3 720	∞
b_i	0.156	0.116	0.131	0.201	0.093	0.303

Table 1: Characteristic values assumed for b_i and τ_i in Eq. (2), based on details found in Refs. [1, 2]. The time constants have been scaled to various temperatures using the Arrhenius equation.

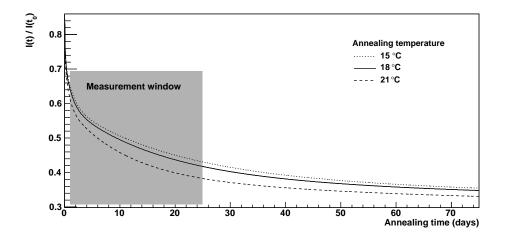


Figure 1: Expected behavior of the bias current during annealing for various temperatures, based on constants in Ref. [1, 2]. The shaded region corresponds to the period when annealing data was recorded for this study.

temperature of 18°C, and also 11°C and -5°C, which are the nominal operating temperatures of the SVX and L00 CDF silicon subdetectors, respectively (see Sec. 3). Note that the weights b_i are not temperature dependent, but the time constants τ_i scale according to the Arrhenius equation.

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Figure 1 shows the expected leakage current behavior during annealing for annealing temperatures of 15, 18 and 21 °C. As can be seen, the leakage current is at its maximum immediately after warming, and then decreases due to the annealing behavior as described in Eq. (2). The shaded region in Fig. 1 corresponds to the period when CDF annealing data were recorded. As the time constants of the individual terms in Eq. (2) span several orders of magnitude, the measurements presented are sensitive to only a subset of the parameters in Eq. (2). A more appropriate parameterization is thus

$$\Delta I(t) = A_I \exp\left(-\frac{t}{\tau_I}\right) + B_I , \qquad (3)$$

where A_I and B_I are empirical constants, and τ_I is a time constant associated with annealing; it is calculated to be 17.6 days, as discussed in Sec. 8.1.

The change in the depletion voltage $V_{\rm dep}$ during annealing occurs in a more complicated fashion. According to the Hamburg model, as the sensor is irradiated

with an accumulated fluence Φ_{eq} , V_{dep} changes proportionally to any adjustments in the effective doping concentration:

$$\Delta N_{\text{eff}} = N_A(\Phi_{ea}, t) + N_C(\Phi_{ea}) + N_Y(\Phi_{ea}, t) \tag{4}$$

where t is the annealing time, and N_A , N_C and N_Y represent contributions from short-term annealing, a stable damage component independent of annealing time, and reverse-annealing, respectively. As we are primarily interested in the time-dependence of annealing, N_C merely serves as an overall offset, and so we do not specify its explicit form. The short-term and reverse annealing components, are given by:

$$N_A(t) = N_A \exp\left(-\frac{t}{\tau_A}\right)$$
 , and (5)

$$N_Y(t) = \begin{cases} N_Y \left(1 - \exp\left(-k_{1Y} t \right) \right) & \text{for first-order process} \\ N_Y \left(1 - \frac{1}{1 + k_{2Y} N_Y t} \right) & \text{for second-order process} \end{cases} . \tag{6}$$

where the dependencies on the fluence Φ_{eq} have been absorbed by the constants N_A and N_Y . An explanation of the definitions and differences of first- and second-order processes can be found in Ref. [1].

At room temperature, reverse-annealing has a time scale on the order of 500 days [1], for which both first- and second-order processes can be approximated for this analysis by a term linear in annealing time: $N_Y(t) \approx N_Y t/\tau_Y$, where τ_Y is the 500-day time constant. We therefore expect the depletion voltage $V_{\rm dep}$ to follow

$$V_{\text{dep}} = V_A \exp\left(-\frac{t}{\tau_V}\right) + V_C + V_Y\left(\frac{t}{\tau_Y}\right) \tag{7}$$

where V_A and V_C represent offsets, and V_Y is the constant associated with reverseannealing. The short-term annealing time constant τ_V is expected to be $3.6^{+2.2}_{-1.3}$ days, based on parameters given in Ref. [1], and scaling to 18 °C using the Arrhenius equation. Note that the value of this time constant is expected to be much less than that of Eq. (3).

To illustrate the temperature dependence on the predicted annealing behavior of $V_{\rm dep}$, we plot the Hamburg-model prediction assuming annealing temperatures of 15, 18, and 21 °C, shown in the top plot of Fig. 2. For these predictions, we use values of V_A , V_C and V_Y based on estimates made specifically for the L00 narrow

ladders of the CDF silicon detector (see Sec. 3). The lower plot of Fig. 2 shows the nominal prediction for the $V_{\rm dep}$ annealing behavior at 18°C, and also shows the uncertainty in that prediction, based on estimates in the model parameters as derived in Ref. [1]. Note that whereas $V_{\rm dep}$ is expected to reach a minimum at some point during annealing, the same behavior is not expected for the leakage current, which decreases monotonically as a function of annealing time.

Assuming the Hamburg model appropriately describes the behavior of the silicon sensors at CDF, we expect the after-before depletion voltage ratio to reach a minimum before the end of the measurement window, reaching a value somewhere between 50% and 65% assuming no annealing prior to the end of Run II. Due to scheduling constraints, we were unfortunately unable to extend the measurement window to distinguish between an asymptote and a minimum.

3. The CDF Silicon Detector

The CDF silicon detector system consisted of three sub-detectors, all with barrel geometry: Layer 00 (L00) [3, 4], the Silicon Vertex detector (SVX) [5] and the Intermediate Silicon Layers (ISL) [6]. Unless otherwise stated, "detector" refers to the CDF silicon detector. Figure 3 presents the schematic layout of the CDF silicon detector. The design had eight silicon *layers* to provide tracking which was robust against failure or degradation of individual components.

The basic structural unit of a sub-detector was a *ladder*, which consisted of several silicon microstrip sensors bonded in series (3 sensors for L00 ladders, 4 in SVX ladders and 6 in ISL ladders). Only L00 sensors and SVX sensors in layers 0, 1 and 3 were used in this study. SVX sensors in layers 2 and 4 developed complicated noise profiles making simple data analysis described in this paper unfeasible. The ISL ladders were located too far from the beamline to receive significant dose of radiation and were therefore not of interest for this study, and are not discussed further in this paper.

The sensors were made from high-resistivity n-type silicon with a nominal thickness of 300 μ m. Sensors in L00 were single-sided, providing r- ϕ information, while sensors in the other layers were double-sided, providing both r- ϕ and r-z information.

A full ladder was read out from both ends through SVX3D readout chips mounted on electrical hybrids, located outside (for L00) or inside (for SVX) of the tracking volume. Multiple readout chips were chained together to read out a single silicon sensor. A circuit board called the *portcard* was located at the periphery of each support structure or bulkhead and formed an interface with the

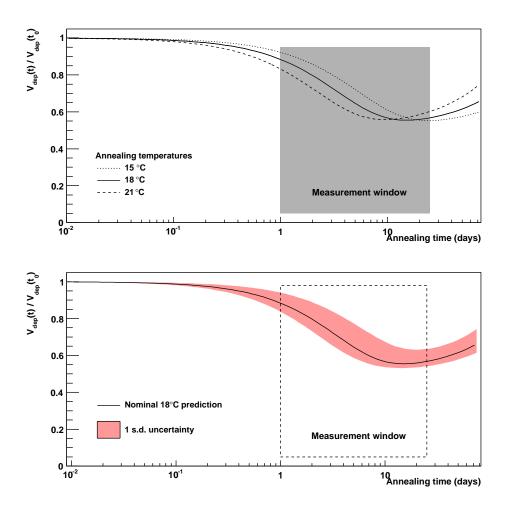


Figure 2: Expected behavior of the depletion voltage $V_{\rm dep}$, assuming the Hamburg model, for various annealing temperatures (top), and for 18°C, but including the one standard-deviation uncertainties on the parameters that are assumed in the model (bottom). For this study, data were collected in the time period indicated by the boxed region.

hybrids and readout chips with the rest of the data acquisition system.

Layer 00 was a single-sided, single-layer silicon microstrip detector whose sensors were grouped into 48 ladders. It was mounted on a carbon fiber support structure which in turn was mounted directly on the beam pipe. The L00 ladders are classified as *narrows* or *wides*, based on the azimuthal extent of the ladder. Narrow ladders were positioned closest to the beam pipe at a radius of 1.35 cm; the wide ladders were located farther away from the beam pipe at a radius of 1.62

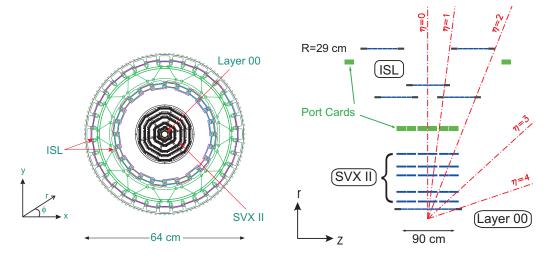


Figure 3: Schematic layout of the CDF silicon detectors showing r- ϕ (left) and r-z (right) views. Note that the z axis is compressed for illustration purposes.

cm.

The SVX detector was built in three cylindrical barrels each 29 cm long. Each barrel contained five layers of double-sided silicon microstrips placed along the beam axis, with radial coverage from 2.5 to 10.7 cm. Carbon fiber reinforced Rohacell foam [7] provided support to the ladders, and beryllium bulkheads provided additional support and alignment on each end. Therefore the detector consisted of six bulkheads (*z*-segmentation), each divided into 12 wedges (φ-segmentation) consisting of 5 layers (*r*-segmentation).

The CDF silicon detector used power supply modules manufactured by CAEN (A509 for SVX and A509H for L00). These custom modules were housed in SY527 mainframe crates located in the corners of the CDF collision hall. One power supply module provided low voltages (2 V and 5 V) to the portcard, low voltages (5–8 V) to the SVX3D chip chains, and high voltage (up to 500 V) to bias the sensors of one wedge of the silicon detector. L00 ladders had two bias voltage lines. One voltage line was connected to one of the three L00 ladder sensors while the second one was used for the other two.

The SVX and L00 sensors shared a common cooling system to remove the heat generated by the readout chips and maintain a constant operating temperature for the silicon sensors. The temperature of the coolant exiting the chiller was -10° C. The SVX sensors were not in close thermal contact with the coolant and their temperatures were not directly monitored. However, by combining the measure-

	L00N	L00W	SVX-L0	SVX-L1	SVX-L3
Number of ladders	12	36	72	72	72
Sensors per ladder	3	3	2	2	2
Distance from detector axis	1.35 cm	1.62 cm	2.54 cm	4.12 cm	8.22 cm
Expected dose	11.5 Mrad	8.7 Mrad	4.5 Mrad	2.2 Mrad	0.76 Mrad
Average I_{bias} per sensor	$200 \mu A$	$250 \mu A$	$500 \mu A$	$400 \mu A$	$300 \mu A$
Power supply limit per line	$3000 \mu A$	$6000 \mu A$	$5000 \mu A$	$5000 \mu A$	$5000 \mu A$
Sensor temperature	-3 °C	-3 °C	11 °C	11 °C	11 °C
Max. operating voltage	500 V	500 V	200 V	200 V	200 V
Final operating voltage	365 V	300 V	170 V	100-130 V	<100 V

Table 2: Number of ladders in each layer, distance from the beamline, estimated radiation dose, as well as operating voltage and bias current at the end of Run II. The L00 ladders are classified as L00N or L00W, based on the whether the ladder is narrow or wide in azimuthal extent, respectively. The current temperature of L00 has a $2.5\,^{\circ}$ C uncertainty and/or variation. The current temperature of SVX has a $5\,^{\circ}$ C uncertainty and/or variation among the sensors.

ments of the ambient and coolant temperatures as a function of time with a finite element thermal model and a dedicated post-run measurement, the temperatures of the sensors during data-taking conditions are estimated to be between 10 and 12°C for SVX. For the L00 sensors, cooling was achieved through thermal contact to aluminum tubes glued to the mechanical supports. The L00 readout chips were distant from the sensors, and cooled by separate cooling lines, allowing a temperature of -2.5°C for the L00 sensors during data taking operations.

The radiation dose the detector was exposed to was estimated using TLD measurements of the radiation field in the CDF tracking volume [8], extrapolated to the location of the individual silicon layers. The equivalent dose from 1 MeV neutrons can be approximated by assuming that the contributions from photons and low-energy neutrons to the TLD measurements are negligible and that the damage is caused primarily by high-energy charged pions. This results in the relations 1 Mrad $\approx 3.9 \times 10^{13}$ high-energy pions/cm² $\approx 2 \times 10^{13}$ 1 MeV neutron/cm² equivalent.

Table 2 provides information about the number of ladders in each layer used in this measurement, distance from the beamline, estimated radiation dose, as well as operating voltage and bias current at the end of Run II. The bias currents in sensors from the same layer vary by 20%, largely due to temperature differences among the sensors. It is worth noting that only one sensor per L00 ladder was used in this measurement because the bias voltage lines connected to two sensors drew too much current to be powered at 18°C.

4. Measurement Procedure

The annealing measurement presented in this paper lasted for 24 days. The results are based on the measurements of *IV* curves of the sensors and the evolution of these curves as a function of annealing time. For this study, the detector was warmed up from operating temperature described in the previous section to 18°C. Higher temperatures were desired for the annealing measurement, but could not be reached due to current in the bias lines exceeding the power supply limit. Because the bias current is very sensitive to the temperature of the ladder, attempts have been made to minimize the effects of ladder self-heating and cross-heating from other ladders used in the study. Unless otherwise stated, detector/ladder OFF state in this paper refers to both high and low voltages set to 0 volts and detector/ladder ON state refers to both high and low voltages set to their nominal values.

4.1. Power Supply Modifications

As stated earlier, CAEN power supply modules provided low voltages to the portcard, low voltages to the SVX3D chip chains, and high voltage to bias the sensors of one wedge of the silicon detector. A safety feature prevented the modules from powering ON the high voltage (HV) channels without first ensuring that the corresponding low voltage (LV) channels are ON. Another safety feature of the power supply modules prevented them to be powered ON when one or more HV or LV cables are disconnected. In other words, in the Run II operating configuration, it was impossible to apply bias voltage without switching on power to the portcards and readout chip chains. When powered, these electronics provided significant heat to the sensors. Therefore, modifications were needed to the power module configuration in order to de-couple the HV and LV channels and apply bias voltage with LV cables disconnected.

4.2. IV scan software

Custom software was developed in order to perform automatic IV scans. A scan consisted of varying the bias voltage from 0 to V_{max} in multiple steps for a particular ladder. The value of V_{max} depended on the detector layer to which the ladder belonged. The step size was also layer dependent and typically in the range of 5–10 V. Not more than two L00 ladders were scanned simultaneously to avoid the effects of cross-heating. Moreover, any ladders scanned in parallel were required to be well separated in the detector volume. For such configurations, the effects of cross-heating were proved to be negligible by observing no change in the bias current for one of the ladders while the other was powered on and off.

4.3. Detector Monitoring

Monitoring software was developed to ensure successful execution of the annealing study. The IV, I_{bias} -vs.-time and V_{bias} -vs.-time curves were stored for each scan and checked each day. Any changes in operating temperature outside the allowed tolerance triggered an alarm.

4.4. Timeline of the Measurement

Preparation for the measurements started on 09/30/2011, the official end of the Tevatron Run II. Dedicated *IV* scans with L00 and SVX low voltage ON were performed. These data help determine the overall change in depletion voltage during the annealing period. L00 and SVX were switched OFF after these scans, and the chiller setpoint temperature raised to -5°C to avoid freezing in the ISL cooling pipes.

On 10/3/2011, the modifications to the power supply modules were completed, and the LV cables were left disconnected from the power supplies until the final day of the study. In parallel, the interlock system settings were changed to allow powering up the detector at temperatures higher than allowed during data taking. The warm-up started on 10/4/2011 and was performed by raising the chiller setpoint temperature in three steps: to 5°C, to 15°C, and to 18°C. The warm-up stages were separated by three hours in time to allow the temperature in the detector volume to stabilize. IV scans were performed at the end of each stage of the warm-up.

From 10/4/2011 to 10/27/2011, stable thermal conditions were maintained, except for two trips of the chiller that regulated the coolant temperature. These trips resulted in colder temperatures for 2-3 hours, and data acquired during these periods were discarded. *IV* scans were performed on groups of 2 L00 ladders at a time, with each ladder being scanned roughly every 21 hours. The temperature of the detector volume and the status of the power supply modules were closely monitored.

On 10/28/2011, the chiller set point was lowered from from 18°C to 9°C to measure bias currents of the SVX ladders at a controlled temperature, uniform across the ladders. These data were used to determine the operating temperature of the SVX ladders reported in Table 2. On 10/30/2011, the chiller set point was lowered to 0°C to provide stable and uniform thermal conditions for the L00 ladders. The bias current of each L00 ladder was measured both at the full operating voltage and half the operating voltage. Only one ladder was powered at a time for maximum thermal stability. These data were used to determine the thermal cou-

pling constants κ for each ladder, needed for the self-heating corrections discussed in Appendix A.

Finally, on 10/31/2011, the LV cables were reconnected to the power supply modules, the chiller setpoint lowered to -10° C, and the LV power turned on. Another set of IV scans under operating (data taking) conditions was recorded. The change in depletion voltage over the annealing period was determined by comparing these scans with those taken before the warmup with the same thermal conditions.

5. Bias Current Evolution

The change in bias current during the annealing period in two ways. We first measure the fractional change over the entire annealing period by comparing the bias current measured under operational thermal conditions before and after the annealing period. Under these thermal conditions, the self heating of the ladders is negligible. Figure 4 shows the ratio of the average ladder current after and before annealing as a function of radial distance from the beam axis. The average is taken over all functional ladders in the layer.

Secondly, we track the bias current evolution over the course of annealing. By examining the current measured at the largest voltage of each IV scan, roughly every 21 hours, the shape of the bias current decrease can be examined. The self-heating of the sensors is substantial under these circumstances, increasing the temperature as much as 3° C. The bias currents have a strong temperature dependence, increasing roughly 10% for every degree increase in temperature [9]. Thus, the measured currents must be corrected back to the equivalent current at 18.2° C before information about the annealing processes can be extracted. It is assumed that the temperature increase is linearly proportional to the power dissipated by the ladder, and the constant of proportionality κ , unique to each ladder, was determined with a dedicated measurement described in Appendix A. Note that only the narrow ladders of L00 are used for the warm-temperature measurements. This is because the wides dissipate 3-4 times more heat due to their larger sensor volume, and they have a weaker coupling to the cooling system, which introduces complications to the self-heating correction procedure.

Figure 5 shows the data for a typical L00 narrow ladder. The measured currents at the full voltage are shown as a function of annealing time with red squares. The blue circles represent the equivalent current at 18.2°C. The annealing time dependence of the corrected currents is fit to Eq. (3), and the fit result is shown as a solid line.

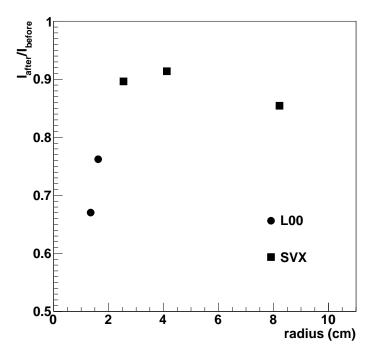


Figure 4: The ratio of the average bias currents for all functional ladders in a single layer, measured after and before annealing, plotted as a function of the distance of the ladders from the central axis of the detector.

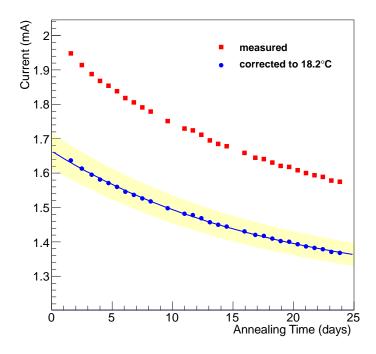


Figure 5: The time constant of this fit is $18.29\pm1.24^{+0.22}_{-0.20}$ days. The first uncertainty is statistical and the second systematic, derived from from the uncertaintly on κ .

An alternate fit function, with an additional exponential term, was considered. However, the uncertainties on the parameters of the additional term were large, suggesting that it is not needed to describe the data. This is discussed further in Sec. 7.

The uncertainty on the parameter κ in the temperature correction determines the shaded uncertainty band shown around the best fit in Fig. 5, and is used to determine the systematic uncertainty on the time constant of the decaying exponential. A compilation of the fit results for all ladders is presented in Sec. 7.

6. Depletion Voltage Changes

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As mentioned in Sec. 2, the behavior of a silicon sensor is partially characterized by the depletion voltage $V_{\rm dep}$, which is the minimum bias voltage that depletes the active detector region of any charge carriers. In the context of test beam setups, the value of $V_{\rm dep}$ is determined by measuring the capacitance of the

sensor as a function of $V_{\rm bias}$. The CV curve exhibits a kink at high voltage, and the intersection of two lines that describe the data before and after the kink unambiguously defines $V_{\rm dep}$. For an operating experiment, the capacitance cannot be measured practically, so an alternate definition of $V_{\rm dep}$ is used, which corresponds to the voltage that maximizes the signal collection in a given data-collection time window (see Ref. [10] for details).

For the annealing study presented here, a signal source (i.e. source of charged particles) was not available, thus requiring an alternate method to determine $V_{\rm dep}$. As the shape of the IV curve is the only feature we have to infer the internal properties of the silicon sensor, we develop a metric called $V_{\rm knee}$, which is a value extracted from a fit to the IV characteristic itself, as discussed in Sec. 6.1.

We use V_{knee} to measure two quantities:

- 1. The relative change in depletion voltage, based on the mapping method described in Sec. 6.2, and
- 2. The evolution of V_{knee} throughout the annealing process, characterized by a time constant as described in Sec. 6.3.

6.1. IV Fit Procedure

For each IV scan taken, the data are fit to a function, motivated by the Shockley formula for a p-n junction [9], and an additional term linear in V_{bias} , which accounts for radiation-damage effects, approximated by a resistor placed in parallel with a p-n junction:

$$I_{\text{bias}}(V_{\text{bias}}; \mathbf{p}) = p_0 - p_1 \exp(-p_2 V_{\text{bias}}^{p_3}) + p_4 V_{\text{bias}},$$
 (8)

where the $\{p_i\}$ represent parameters to be fitted. The *IV* data are fit using 80% of the voltage range, so as to minimize potential residual self-heating effects that can occur at the largest voltages. The I_{bias} uncertainties assumed correspond to half of the spread of the measured bias current for a given voltage setting, after effects from self-heating have stabilized. Typically this uncertainty is on the order of a few μ A. An additional uncertainty of 1 μ A, corresponding to the uncertainty of the least-significant bit, is added in quadrature to this spread.

We define the knee voltage $V_{\rm knee}$ as the voltage where the slope of the fit reaches 5% of its maximum value, relative to the difference of the maximum and minimum slopes. The uncertainty in $V_{\rm knee}$ is determined by propagating the uncertainties on the fit parameters (assumed to be Gaussian-distributed about their central values), using a pseudoexperiment study that accounts for the fit-parameter correlations.

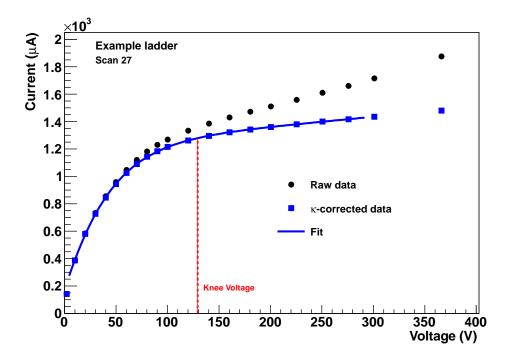


Figure 6: The fit of a warm IV scan for an example ladder, corrected for self-heating effects. Also shown are the uncorrected data points in black.

Figure 6 shows an example fit for one of the L00 ladders. The original, uncorrected data points are shown as black circles, and the κ -corrected points as blue squares. The IV fit is performed on the κ -corrected IV points, using the parameterization of Eq. (8). Also shown is the extracted knee voltage, and its associated uncertainty (which is on the order of a few V, and thus difficult to see for this particular fit). As illustrated in Fig. 6, the effect of the self-heating correction κ (Appendix A) can be significant at higher voltage values. This behavior is not observed for low-voltage ON IV scans, which were performed at low temperatures. The κ corrections are thus omitted for the analysis of the LV ON scans, which are described in Sec. 6.2.

6.2. Overall Relative Change in V_{dep}

As mentioned in Sec. 4.4, separate low-voltage (LV) ON scans, corresponding to operating conditions, were taken before and after the annealing period. Although we have no signal source to infer $V_{\rm dep}$ values for these scans, we develop a map between $V_{\rm knee}$ and $V_{\rm dep}$ for the L00 ladders. To construct such a map, we

use information from the scans performed during nominal CDF running that determined the actual depletion voltage $V_{\rm dep}$, and the corresponding $V_{\rm knee}$ value derived from fits to the IV data using the same functional form as shown in Eq. (8). We take the extracted $V_{\rm knee}$ values and plot them against the measured $V_{\rm dep}$ values, and then use an analytical expression to relate the two sets of values.

Whereas the $V_{\rm knee}$ determination of each nominal-running scan is usually reliable, the $V_{\rm dep}$ value often includes large uncertainties. This is a result of increased radiation damage on the silicon ladder as a function of integrated luminosity. We therefore assume a simple linear parameterization $V_{\rm dep} = p_0 + p_1 V_{\rm knee}$, which associates a given value of $V_{\rm knee}$ to $V_{\rm dep}$. Some sample mappings are given in Fig. 7.

Using these linear mappings, we take the measured $V_{\rm knee}$ values from the LV ON scans before and after annealing and associate them with mapped depletion voltages $\widetilde{V}_{\rm dep}$. We then infer the decrease in mapped depletion voltage, by forming the ratio $\widetilde{V}_{\rm dep}/\widetilde{V}_{\rm dep}^0$, where $\widetilde{V}_{\rm dep}^0$ corresponds to the mapped depletion voltage before annealing. This quantity is measured for eleven L00 ladders, and the result from each ladder is combined into a global average, presented in Sec. 7.

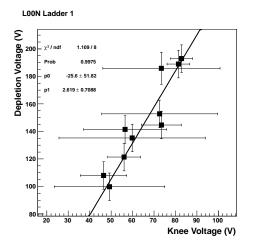
Note that by using these mappings, we assume that:

- 1. $V_{\rm knee}$ serves as a reliable metric of the IV curves that associates the IV data to a unique value of the depletion voltage $V_{\rm dep}$, and
- 2. as a mapping can be made only of pre-annealed data, we assume that the behavior of $V_{\rm knee}$ before annealing corresponds to its behavior afterward.

$6.3. V_{\rm knee}$ Evolution

Although it would have been desirable to track the evolution of $V_{\rm dep}$ over the course of the full month of annealing, the $V_{\rm knee}$ - $V_{\rm dep}$ maps as described in Sec. 6.2 cannot be used due to non-trivial temperature dependences of $V_{\rm knee}$. To use the maps, the silicon sensor temperatures would have had to be lowered to nominal-running temperatures after each IV scan was taken, which was impractical. Instead, we track the evolution of the knee voltage during the annealing period.

Figure 8 shows the $V_{\rm knee}$ values, associated with fitting the IV curves over the course of a month, for one of the L00 ladders. To extract an overall time constant, we assume the $V_{\rm knee}$ evolution follows Eq. (7), and we fit for V_A , V_C , and the time constant τ_V —we omit the reverse-annealing term proportional to V_C as we observe no increase in $V_{\rm knee}$ at larger values of annealing time. The solid line in Fig. 8 corresponds to the best fit using the Eq. (7) parameterization without the reverse-annealing term, resulting in a fitted time constant of $\tau_V = 6.14 \pm 0.38$ days.



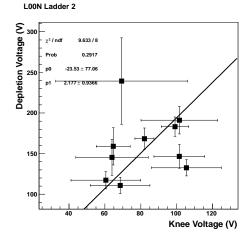


Figure 7: $V_{\rm knee}$ - $V_{\rm dep}$ mappings for two L00 narrow ladders. The uncertainties in both directions are taken into account using the "effective variance" fit method. In general, the χ^2 /ndf is very consistent with unity. The plot on the right shows the poorest example of any obvious mapping between $V_{\rm dep}$ and $V_{\rm knee}$.

Systematic uncertainties due to the self-heating correction κ are incorporated by varying κ by its uncertainty in the positive and negative directions, and refitting the κ -corrected *IV* data. The fits corresponding to varying κ are shown as a red, shaded region in Fig. 8. From these alternate fits, we extract the corresponding time constants and assign the maximum deviation from τ_V as the systematic uncertainty on the result. For the L00 ladder shown, the systematic uncertainty is 0.07 days, giving $\tau_V = 6.14 \pm 0.38 \pm 0.07$ days. This analysis is repeated for eleven L00 ladders. A weighted average of the time constants from the eleven L00 ladders is performed and shown in Sec. 7.

7. Results of the Annealing Study

Figure 9 displays the fitted time constant of the bias current evolution shape for each narrow ladder, as described in Sec. 5. The individual results are consistent with a single time constant, suggesting that potential differences among the ladders due to (e.g.) annealing temperature or radiation dose variations are small. The weighted average of $\tau_I = 17.88 \pm 0.36 \pm 0.25$ days is obtained by assuming uncorrelated statistical uncertainties and fully correlated systematic uncertainties due to the self-heating corrections, as described in Appendix A. The total uncertainty on the combined result is indicated with a shaded band.

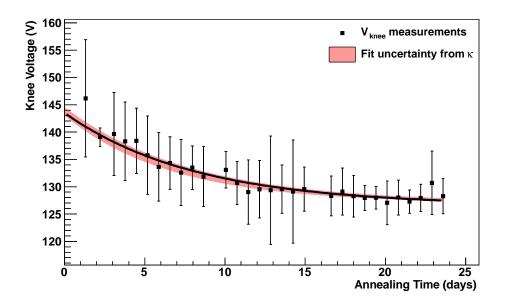


Figure 8: Exponential fit to V_{knee} values that include the systematic uncertainty from the κ values.

Figure 10 shows the after-before ratio in mapped depletion voltage $V_{\rm dep}$, as described in Sec. 6.2, for each of the L00 ladders considered as well as a weighted average. The uncertainties of each $\widetilde{V}_{\rm dep}$ determination are assumed to be uncorrelated. The average after-before $\widetilde{V}_{\rm dep}$ ratio is thus 0.73 ± 0.03 , indicating an average reduction of roughly 25% in $V_{\rm dep}$ due to annealing.

Finally, we present the averaged result for the $V_{\rm knee}$ evolution time constant in Fig. 11, as described in Sec. 6.3. The results is an average of $\tau_{\rm avg} = 6.21 \pm 0.18$ days. The statistical uncertainties are again assumed to be uncorrelated, whereas the systematic uncertainties due to κ are treated as fully correlated in the weighted average. The solid vertical line and red, shaded region represent the weighted average and its total uncertainty, respectively.

8. Comparison with the Hamburg Model

In order to compare these measurements with the Hamburg Model, the fraction of annealing that happened during the run must first be estimated using the record of sensor temperatures as a function of time. The temperatures of SVX and L00 sensors during data taking operations were roughly 11°C and -2.5°C, respectively.

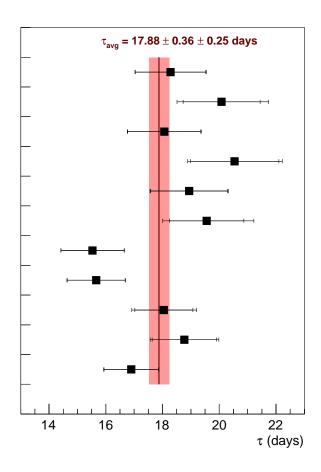


Figure 9: Time constants for decrease in temperature-corrected current corresponding to $V_{\rm op}$.

During shutdowns, with the heat load from the readout chips removed from the system, temperature of both the SVX and L00 sensors was 0-5°C.

During data-taking operations, the SVX sensors were sufficiently warm that annealing and irradiation happened simultaneously throughout the run. Annealing during shutdown periods was negligible compared with operations for the SVX sensors for both bias current and depletion voltage. The L00 sensors were colder during operations. Figure 8.1 shows the average measured cooling line temperature for each day as a function of time during the run, with shutdowns indicated with shaded bands. After the first inverse femtobarn of integrated luminosity, the coolant temperature was kept below 5°C even during shutdowns. During shutdowns, the sensor temperature was very close to the coolant temperature as the

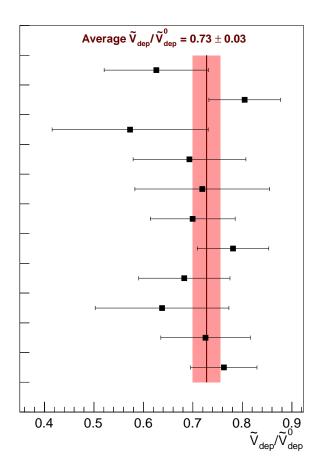


Figure 10: $\widetilde{V}_{\rm dep}/\widetilde{V}_{\rm dep}^0$ determinations for the eleven narrow L00 ladders, along with the weighted average.

readout chips were not powered and therefore the heat load on the system was significantly reduced. During operations, the sensor temperature is estimated to be 2.5°C warmer than the coolant, or -2.5°C on average. A few small spikes in the daily average not associated with a shutdown period are visible and result from excursions for a few hours to warmer temperatures during short interruptions in coolant circulation.

8.1. Bias Current Evolution

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The total change in bias current expected during the annealing period can be predicted from Eq. (2) and compared to the observed values reported in Fig. 4. The

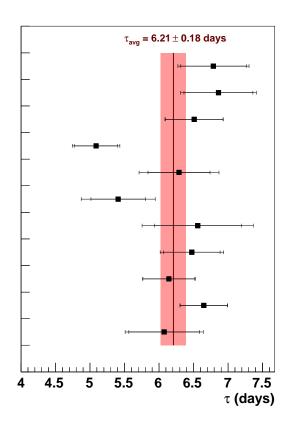


Figure 11: Annealing time constants for the eleven narrow L00 ladders, and the weighted average, assuming the measurements are uncorrelated.

first three terms in Eq. (2) have time constants of 24 minutes, 2.7 hours and 1.0 days at 18°C, or equivalently 18 hours, 5.0 days, and 46 days at -5°C, respectively. Thus, the bias current reduction due to annealing represented by the first two terms happened during operations in a continuous fashion for L00. The contribution from the third term is also anticipated to be negligible, considering that less 10% of the total radiation dose was delivered in the last 90 days of Run II. Considering only the remaining three terms, the after-before ratio of bias currents is expected to be 68% for 24 days of annealing at 18°C, consistent with the measured values for the L00 sensors. For the SVX sensors, only the last two terms are relevant for the annealing study, and 94% is expected for the after-before ratio of the bias currents, again consistent with the measured values.

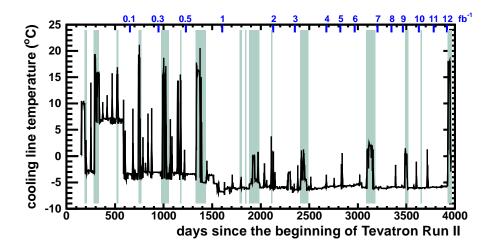


Figure 12: The average cooling line temperatures for L00 during Run II. Shutdowns are indicated with shaded bands. Some values of of integrated luminosity are indicated along the top of the plot at the time they were reached, but there is not a linear correspondence between time and luminosity.

Similarly, we can compare the observed exponential decay of the bias current with expectations. An experiment was simulated by calculating expected measured currents with a modified version of Eq. (2) for the annealing time patterns of the actual measurements, every 21 hours starting 1.5 days after warmup. Using the considerations of the previous paragraph, Eq. (2) was modified by setting the amplitudes of the first three terms, b_1, b_2 and b_3 equal to 0.

The upper plot of Fig. 8.1 shows with a dashed line, the expected measured bias currents as a function of annealing time from the modified Eq. (2). In both plots, the circles represent the expected measurements calculated by sampling the dashed line every 21 hours starting at 1.5 days. The solid line in the lower plot is the best fit to the sampled currents using Eq. (3), giving a time constant of 17.58 days, which is in good agreement with the measured value of $17.88 \pm 0.36 \pm 0.25$ days.

8.2. Depletion Voltage Evolution

For the depletion voltage time constant, we expected $\tau_V = 3.6^{+2.2}_{-1.3}$ days. Our measurement of 6.21 ± 0.18 days exceeds this prediction by just over one standard deviation. Due to the complicated dependence on the various model parameters, it is difficult to compare the measured after-before \widetilde{V}_{dep} ratio of $(73 \pm 3)\%$ to the

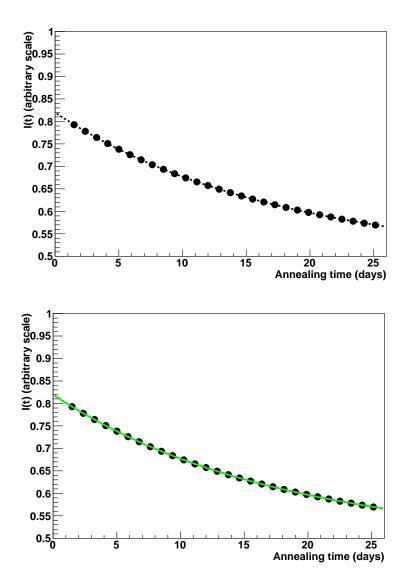


Figure 13: A simulated measurement of the decaying bias currents. The upper plot shows with a dashed line the function used to generate the points, a modified version of Eq. (2) with the amplitudes $b_{1,2,3}$ set to zero. The green solid line in the lower plot is the best fit result assuming Eq. (3).

Hamburg model-prediction, which is somewhere between 50% and 65%, as mentioned in Sec. 2. It does appear that our measurement exceeds that of the Hamburg prediction to some extent, which would be consistent with an assumption that annealing happened to some degree before the end of Run II.

9. Summary

After 12 fb⁻¹ of integrated luminosity, the CDF Run II silicon detector was annealed at 18 °C for 24 days. The ratio of the bias currents after and before annealing is a measure of how much each subsystem annealed during the run. The overall change and evolution of the bias currents, depletion voltages, and $V_{\rm knee}$ values of several sensors during the annealing process were measured.

For L00, we observed a decrease of the bias current and depletion voltage of the heavily irradiated sensors, with time constants consistent with the Hamburg model expectations. We observed no indication of the reverse annealing on this time scale.

In contrast, the bias currents of SVX changed very little during the annealing period. This confirms that these sensors, with a significantly warmer operating temperature than L00, annealed while operating. As the operating temperature of the SVX sensors was not directly measured, and the prediction of their temperature from finite element thermal models has large uncertainties, this is an important confirmation of the annealing during the run, which extended the lifetime of the detector.

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Appendix A. Temperature corrections for dissipative heating

The temperature dependence of the bias current for a fully depleted, reverse-biased diode is well understood [9]. If the current I_0 is measured at a known temperature T_0 , then the current at temperature T is given by

$$\frac{I(T)}{I_0} = \left(\frac{T}{T_0}\right)^2 \exp\left[\frac{E_g}{2k_B} \left(\frac{1}{T_0} - \frac{1}{T}\right)\right] \tag{A.1}$$

where k_B is Boltzmann's constant and $E_g = 1.21 \pm 0.06$ eV is the effective band gap energy [11].

The temperature of a biased sensor increases as a result of resistive power dissipation in the sensor, hereafter referred to as "self-heating". For the *IV* scans of the annealing period, this temperature increase was as large as 3°C for the L00 narrow sensors at the largest voltages. Such a temperature shift can result in bias current deviations from nominal 18.2°C values by as much as 30%. These temperature variations must be removed from a set of measured currents. before information about the annealing processes can be extracted.

The measured currents can be corrected to a common temperature using Eq. (A.1) if the sensor temperature at the time of the measurement is known. The sensor temperature is not measured directly, however it can be calculated from the measured current I at a given bias voltage V, assuming that the temperature increases linearly with the power dissipated by the ladder:

$$T = 18.2^{\circ} \text{C} + \kappa IV \tag{A.2}$$

where κ is a proportionality constant.

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Using values of κ determined with a dedicated measurement, a temperature correction was applied to the measured currents before extracting the knee voltage, as shown in Fig. 6, and the time constant of the current decay, shown in Fig. 5.

Appendix A.1. Determination of K for each sensor

The value of κ for each sensor was determined by combining data from a dedicated reference current measurement with the last set of IV scans taken during the annealing period. A collection of reference current measurements were taken at the operating voltage $V_{\rm op}$ and at half of the operating voltage $0.5 \times V_{\rm op}$ after the 24-day annealing period. For these measurements, the readout chips were not powered and the measured cooling line temperatures were between -0.5 and 1.1°C for the system. Only one sensor was biased at a time to minimize any potential heat load

The expected bias current I'_{ref} at the annealing temperature $T_a = 18.2^{\circ}$ C, in the absence of self heating, can be calculated from the measured reference currents I_{ref}

$$I'_{\text{ref}} = I_{\text{ref}} \left(\frac{T_a}{T_{\text{ref}}} \right)^2 \exp \left[\frac{E_g}{2k_B} \left(\frac{1}{T_{\text{ref}}} - \frac{1}{T_a} \right) \right]$$
 (A.3)

where T_{ref} is the temperature of the sensor during the measurement of the reference currents.

The last set of IV scans taken during the annealing period contain bias currents measured in this voltage range. Self heating increases the temperature of the sensor to a value $T_{\text{hot}} = 18.2^{\circ}\text{C} + \kappa IV$, and the measured current I_{meas} can be corrected to the equivalent current I_{corr} at the annealing temperature $T_a = 18.2^{\circ}\text{C}$ by

$$I_{\text{corr}} = I_{\text{meas}} \left(\frac{T_a}{T_{\text{hot}}}\right)^2 \exp\left[\frac{E_g}{2k_B} \left(\frac{1}{T_{\text{hot}}} - \frac{1}{T_a}\right)\right]. \tag{A.4}$$

Because there are two reference current measurements at two different bias voltages for each sensor, unique values of $T_{\rm ref}$ and κ for each sensor are determined by requiring that $I_{\rm corr} = I'_{\rm ref}$ for both measurements. This initial result is then improved upon iteratively by correcting the reference currents for a small amount of self heating, using the values of κ and $T_{\rm ref}$ from the previous iteration in Eqs. (A.2) and (A.1). For all ladders, the values of κ and $T_{\rm ref}$ converge in fewer than four iterations. The difference between the initial and final values of κ is taken as a systematic uncertainty.

Figure A.14 illustrates this process for a typical narrow L00 ladder. The measured currents I_{meas} from the IV scan are shown as solid circles, and the corrected currents I_{corr} as solid squares. The values of κ and T_{ref} from the first iteration are

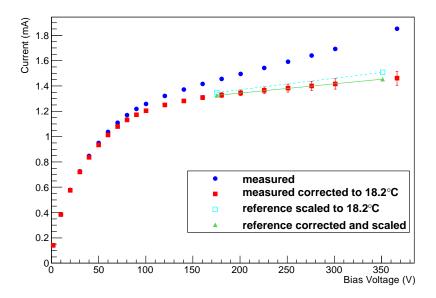


Figure A.14: The measured bias current as a function of bias voltage for a typical L00 narrow ladder is plotted with blue circles. The equivalent current at 18.2 °C is shown with solid red squares. The open cyan squares show the reference measurement scaled to 18.2 °C and the green triangles the reference measurement first corrected for self-heating and then scaled to 18.2 °C. The values $\kappa = 3.925$ K/W and $T_{\rm ref} = -0.4$ °C give the best agreement between the red squares and the solid green line for this ladder.

those for which the solid squares agree with the dashed line, while the final values give agreement with the solid line connecting the corrected reference currents shown with solid triangles. If the bias voltage value of the *IV* scan points do not exactly match those of the reference measurement, as in the example illustrated, then the two *IV* scan points closest to the reference scan measurements are chosen, and a linear interpolation is used to obtain measured reference currents for those voltages.

The distributions of κ and $T_{\rm ref}$ are shown in Figs. A.15 and A.16. These values are expected to vary slightly with the ladder location and thermal connection to the cooling lines. $T_{\rm ref}$ and κ have common systematic errors from the uncertainties on the absolute annealing temperature $T_{\rm warm} = 18.2 \pm 0.5$ °C and the effective gap energy $E_g = 1.21 \pm 0.06$ eV [11]. For κ , these two uncertainties combine to give an overall uncertainty of 0.05 K/W. The time delay between the last warm IV scan and the reference measurement is different for each ladder, varing from 3 to 24

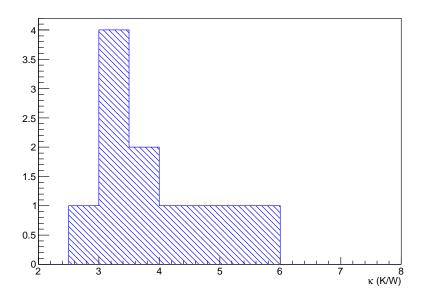


Figure A.15: The distribution of κ for the L00 narrow ladders.

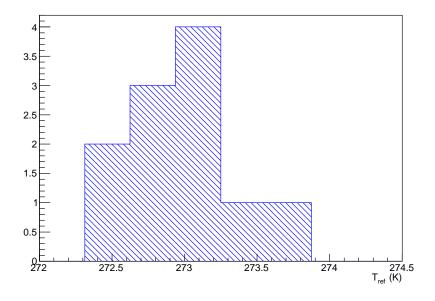


Figure A.16: The distribution of $T_{\rm ref}$ for the L00 narrow ladders.

hours. The current at fixed temperature and voltage decreases due to annealing during this time, but the change is observed to be less than 0.5% for the longest time period and the resulting shifts in $T_{\rm ref}$ and κ are negligible. An important verification of this self-heating correction method comes from the agreement of the best fit values of $T_{\rm ref}$ with the expectation $T_{\rm ref} = 273.0 \pm 1.0 \pm 1.0$ K derived from the measured cooling line temperatures.

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